

06/28/01

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

## U.S. UTILITY Patent Application

<p>O.I.P.E.</p> <p>SCANNED <u>103</u> Q.A. <u>cm</u></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/895559	CONT/PRIOR	CLASS 714	SUBCLASS 7/8	ART UNIT 2133	EXAMINER D. B. [Signature] YUFA [Signature]
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## APPLICANTS

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ASD

TITL

# Testing architecture for a semiconductor memory device

PTO-2040  
12/99

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ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	_____ (Primary Examiner) (Date)			
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